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**Term:** (connector adj board) same (printed circuit board or "PCB") and @pd<19961029

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result set

DB=USPT,PGPB; PLUR=YES; OP=ADJ

<u>L6</u>	(connector adj board) same (printed circuit board or "PCB") and @pd<19961029	242	<u>L6</u>
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DB=USPT; PLUR=YES; OP=ADJ

<u>L5</u>	L4 and @pd<19961029	242	<u>L5</u>
<u>L4</u>	(connector board) same (printed circuit board or "PCB")	372	<u>L4</u>
<u>L3</u>	L1 and @pd<19961029	535	<u>L3</u>
<u>L2</u>	L1 and @pd19961029	0	<u>L2</u>
<u>L1</u>	(connect\$3 board) same (printed circuit board or "PCB")	809	<u>L1</u>

END OF SEARCH HISTORY

**WEST**

Generate Collection

L6: Entry 56 of 242

File: USPT

Aug 17, 1993

DOCUMENT-IDENTIFIER: US 5237567 A  
TITLE: Processor communication bus

DATE ISSUED ((Oracle) (1):  
19930817

Detailed Description Paragraph Right (4):

Like interface units are included in the remaining multi-processor units, though not illustrated in detail, and like interface units are included in each of the remaining memory units, also not illustrated in FIG. 1. As those skilled in the art appreciate, the number of multi-processor units and memories may be increased or decreased in number from that shown in any alternative embodiment as need for greater or lessor numbers of modules dictates. Other computer type resources may also be connected to the bus for interactive communication with the afore described multi-processor and memory type computer resources. Typically, in practice each multi-processor unit and memory unit is formed upon a single printed circuit board. In turn, each printed circuit board contains a connector by means of which such board is electrically connected to an additional board containing the communications bus.